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BOX PCT
IN THE UNITED STATES DESIGNATED OFFICE
OF THE UNITED STATES PATENT AND TRADEMARK OFFICE
UNDER THE PATENT COOPERATION TREATY-CHAPTER II

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INFORMATION DISCLOSURE STATEMENT

APPLICANTS: Rass et al.
ATTORNEY DOCKET NO. P01,0240
INTERNATIONAL APPLICATION NO: PCT/~~EP~~^{EP}99/10474
INTERNATIONAL FILING DATE: December 29, 1999

10 INVENTION: "METHOD AND DEVICE FOR ADAPTIVELY
MODIFYING THE CHARACTERISTICS OF ONE-
DIMENSIONAL SIGNALS"

Assistant Commissioner for Patents,
Washington, D.C. 20231

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S I R:

In accordance with the provisions of 37 C.F.R. § 1.56, Applicant requests that citation and examination of the following documents be made during the course of examination of the above-referenced application for United States Letters Patent.

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AA United States Patent No. 5,732,386

AB United States Patent No. 5,539,412

AC United States Patent No. 5,073,964

AD United States Patent No. 4,446,530

AE United States Patent No. 3,894,195

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AC European Application 0 535 893

AR "Fast Hartley Transform Implementation on DSP Chips," Probhut et al., Microprocessors and Microsystems, Vol. 20, No. 4 (1996), pp. 233-240

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EXPLANATION OF RELEVANCE

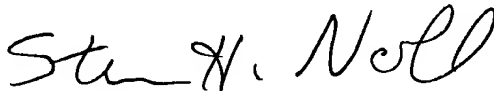
Reference AD was identified and discussed in the present specification, and the remaining references were cited during earlier examination of the present PCT application.

5 Copies of each of the above references together with Form 1449 are submitted herewith.

 This Information Disclosure Statement is being filed simultaneously with the present PCT application, and is therefore in compliance with 37 C.F.R. §1.97(b)(1), and no fee is necessary.

10 All claims of the application are submitted to be patentable over the teachings of the above references, taken singly or in combination. Early consideration of the application is therefore respectfully requested.

Submitted by,



(Reg. 28,982)

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37 CFR 1.501 INFORMATION DISCLOSURE STATEMENT IN A PATENT (use several sheets if necessary)					Docket No. P01,0240		Serial No. New Application	
					Applicant Rass et al.			
					Filing Date Herewith		Group Art Unit Not Yet Assigned	
U.S. PATENT DOCUMENTS								
Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing Date If appropriate	
	AA	5,732,386	03/24/98	Park et al.				
	AB	5,539,412	07/23/96	Mendelson				
	AC	5,073,964	12/17/91	Resnikoff				
	AD	4,446,530	05/01/84	Tsuboka				
	AE	3,894,195	07/08/75	Kryter				
	AL							
	AG							
	AH							
	AI							
	AJ							
	AK							
FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AL	0 535 893	04/07/93	Europe				
	AM							
	AN							
	AO							
	AP							
	AQ							
OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	AR	"Fast Hartley Transform Implementation on DSP Chips," Probhu et al., Microprocessors and Microsystems, Vol. 20, No. 4 (1996), pp. 233-240						
	AS							
	AT							
	AU							
	AV							
	AW							
	AX							
	AY							
	AZ							
Examiner				Date Considered				
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								